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FPGA based Simulation of Clock Gated ALU Architecture with Multiplexed Logic Enable for Low Power Applications Mahaveer Singh Sikarwar¹ Sudha Nair² Manish Trivedi³ 1PG Student 2Assistant Professor 3Associate Professor 1,2,3Department of Electronics & Communication Engineering 1,2,3RKDF Institute of Science & Technology, Bhopal, Madhya Pradesh

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SO WE HAVE A TRADING ROOM - MOVING BEYOND THE WOW! Michael D Evans, Winthrop University, evansm@winthrop.edu Charles Alvis, Winthrop University, alvisc@winthrop.edu ABSTRACT Trading Rooms have proliferated at colleges and universities around the country They are viewed as a potent recruiting tool for a business school

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